

VLSI IEEE PAPERS-2021

S.NO	TITLES	YEAR	CATEGORY
1	Fast Binary Counters and Compressors Generated by Sorting Network	2021	LOW POWER
2	Accuracy-Configurable Radix-4 Adder With a Dynamic Output Modification Scheme	2021	LOW POWER
3	AxBMs: Approximate Radix-8 Booth Multipliers for High-Performance FPGA-Based Accelerators	2021	LOW POWER
4	Design and Analysis of Approximate Compressors for Balanced Error Accumulation in MAC Operator	2021	LOW POWER
5	Design of ultra-low power consumption approximate 4- 2 compressors based on the compensation characteristic	2021	LOW POWER
6	Low-Power Ternary Multiplication Using Approximate Computing	2021	LOW POWER
7	Constant-time Synchronous Binary Counter with Minimal Clock Period	2021	LOW POWER
8	A Low-Power Timing-Error-Tolerant Circuit by Controlling a Clock	2021	LOW POWER
9	A Novel Scheme for Real-Time Max/Min-Set- Selection Sorters on FPGA	2021	IMAGE PROCESSING
10	High Performance CNN Accelerators Based on Hardware and Algorithm Co-Optimization	2021	SIGNAL PROCESSING.
11	Approximate Pruned and Truncated Haar Discrete Wavelet Transform VLSI Hardware for Energy-Efficient ECG Signal Processing	2021	BIOMEDICAL
12	Multi-Context TCAM-Based Selective Computing: Design Space Exploration for a Low-Power NN	2021	LOW POWER
13	Functional Constraints in the Selection of Two-Cycle Gate-Exhaustive Faults for Test Generation	2021	TESTING
14	Padding of LFSR Seeds for Reduced Input Test Data Volume	2021	TESTING

15	Partitioning Functional Test Sequences Into Multicycle Functional Broadside Tests	2021	TESTING
16	Single Test Type to Replace Broadside and Skewed-Load Tests for Transition Faults	2021	TESTING
17	PRESERVE: Static Test Compaction that Preserves Individual Numbers of Tests	2021	TESTING



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